

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Combs et al.

Examiner: ABRAHAM, ESAW T

S/N. No.:10/708/342

Group Art Unit: 2133

Filed.: February 25, 2004

Docket. No.: BUR920030200US1

Title: **TESTING AND REPAIR METHODOLOGY FOR MEMORIES HAVING  
REDUNDANCY**

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Honorable Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**OFFICE ACTION RESPONSE**

This communication is in response to the Office Action mailed September 19, 2006.